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**INFORMATION DISCLOSURE
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(Use as many sheets as necessary)

Complete if Known

Sheet	1	of	1	Application Number	10/743,527
				Filing Date	December 22, 2003
				First Named Inventor	Xu
				Group Art Unit	2841
				Examiner Name	(not yet assigned) HOA C. NGUYEN
				Attorney Docket Number	042933/270304

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages of Relevant Figures Appear

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Foreign Patent Document Country Code - Number Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	English Language Translation Attached
HL	1	EP - 1 261 028 A2	11-27-2002	Hitachi, Ltd.		

OTHER DOCUMENTS

Examiner Initials	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	English Language Translation Attached
HL	2	TAKAGI <i>et al.</i> , "Development of Sequential Build-Up Multilayer Printed Wiring Boards in Japan," <i>IEEE Electrical Insulation Magazine</i> , 2003, Vol. 19(5), pp. 27-56.	
HL	3	WATANABE <i>et al.</i> , "The New Manufacturing Process for Flat-Surface Printed Wiring Boards and the Applications," Proceedings of the Electronic Circuits World Convention, Tokyo, 1999, pp. T4-1-1 - T4-1-8.	

Examiner Signature	Hoanguyen	Date Considered	12/9/05
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. RTA01/2182183v1